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Next assume  $A=1$  and  $C1=0$  On the arrival of an idle slot in the downstream direction, 1) Place the frame in the idle slot, setting the busy bit; 2) If there is a waiting frame in the supplementary queue, put it in the virtual queue, place  $C2$  in counter 1 and set  $C2$  to 0; 3) If there is no waiting frame, set  $A=0$  On the arrival of a request bit in the upstream direction, increment  $C2$

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